

AMENDMENT B (IN RESPONSE TO PAPER NO. 8
(OFFICE ACTION DATED SEPTEMBER 24, 2003))

CLAIM AMENDMENTS

- B1
1. (ORIGINAL) An apparatus including a signal multiplexor for controlling and multiplexing video image and on-screen-display (OSD) signals, comprising:
- a first control circuit that following reception of a first reference signal, a contrast control signal and a clamped video signal provides a first controlled signal with a contrast-controlled video component;
 - a first signal combining circuit, coupled to said first control circuit, that in response to a first combining control signal receives and selectively combines an OSD signal and said first controlled signal to thereby provide a first combination signal with said contrast-controlled video component and an OSD component;
 - a second control circuit, coupled to said first signal combining circuit, that following reception of said first combination signal, said first reference signal and a gain control signal provides a second controlled signal with a contrast-controlled and gain-controlled video component and a gain-controlled OSD component; and
 - a second signal combining circuit, coupled to said second control circuit, that in response to a second combining control signal receives and selectively combines said second controlled signal and a second reference signal to thereby provide a multiplexed signal with said contrast-controlled and gain-controlled video component, said gain-controlled OSD component and a reference signal component.
2. (ORIGINAL) The apparatus of claim 1, further comprising a clamp circuit, coupled to said first control circuit, that following reception of said first reference signal, a clamp control signal, said first controlled signal and an input video signal provides said clamped video signal.

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3. (ORIGINAL) The apparatus of claim 2, wherein said clamp circuit comprises:

an input stage that following reception and combination of a switched clamp signal and said input video signal provides said clamped video signal;

a comparator circuit, coupled to said first control circuit, that following reception and comparison of said reference signal and said first controlled signal provides a clamp signal; and

a switch circuit, coupled between said comparator circuit and said input stage, that in response to said clamp control signal receives and switches said clamp signal to thereby provide said switched clamp signal.

4. (ORIGINAL) The apparatus of claim 1, wherein said first signal combining circuit comprises a multiplexor circuit that multiplexes said OSD signal and said first controlled signal in response to said first combining control signal to thereby provide said first combination signal.

5. (ORIGINAL) The apparatus of claim 1, wherein said second signal combining circuit comprises a multiplexor circuit that multiplexes said second and third controlled signals in response to said second combining control signal to thereby provide said multiplexed signal.

[6-8. (CANCELLED)

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9. (ORIGINAL) A method of controlling and multiplexing video image and on-screen-display (OSD) signals, comprising the steps of:

receiving a first reference signal, a contrast control signal and a clamped video signal and in response thereto generating a first controlled signal with a contrast-controlled video component;

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receiving a first combining control signal and in response thereto receiving and selectively combining an OSD signal and said first controlled signal and thereby generating a first combination signal with said contrast-controlled video component and an OSD component;

receiving said first combination signal, said first reference signal and a gain control signal and in response thereto generating a second controlled signal with a contrast-controlled and gain-controlled video component and a gain-controlled OSD component; and

receiving a second combining control signal and in response thereto receiving and selectively combining said second controlled signal and a second reference signal and thereby generating a multiplexed signal with said contrast-controlled and gain-controlled video component, said gain-controlled OSD component and a reference signal component.

10-11. (CANCELLED)

⁷/₁₂ (ORIGINAL) An apparatus including a signal multiplexor for controlling and multiplexing video image and on-screen-display (OSD) signals, comprising:

a first control circuit that following reception of a first reference signal, a contrast control signal and a clamped video signal provides a first controlled signal with a contrast-controlled video component;

a first signal combining circuit, coupled to said first control circuit, that in response to a first combining control signal receives and selectively combines an OSD signal and said first controlled signal to thereby provide a first combination signal with said contrast-controlled video component and an OSD component;

a second control circuit, coupled to said first signal combining circuit, that following reception of said first combination signal, said first reference signal and

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a gain control signal provides a second controlled signal with a contrast-controlled and gain-controlled video component and a gain-controlled OSD component;

a second signal combining circuit, coupled to said second control circuit, that in response to a second combining control signal receives and selectively combines said second controlled signal and a second reference signal to thereby provide a multiplexed signal with said contrast-controlled and gain-controlled video component, said gain-controlled OSD component and a reference signal component; and

a variable filter circuit, coupled to said second signal combining circuit, that in response to a plurality of filter control signals selectively filters said multiplexed signal to thereby provide a filtered multiplexed signal with said contrast-controlled and gain-controlled video component and said gain-controlled OSD component having enhanced high frequency signal magnitudes.

⁸/₁₃. (ORIGINAL) The apparatus of claim ⁷/₁₂, further comprising a clamp circuit, coupled to said first control circuit, that following reception of said first reference signal, a clamp control signal, said first controlled signal and an input video signal provides said clamped video signal.

⁹/₁₄. (ORIGINAL) The apparatus of claim ⁸/₁₃, wherein said clamp circuit comprises:

an input stage that following reception and combination of a switched clamp signal and said input video signal provides said clamped video signal;

a comparator circuit, coupled to said first control circuit, that following reception and comparison of said reference signal and said first controlled signal provides a clamp signal; and

a switch circuit, coupled between said comparator circuit and said input stage, that in response to said clamp control signal receives and switches said

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clamp signal to thereby provide said switched clamp signal.

~~10~~¹⁰. (ORIGINAL) The apparatus of claim ~~12~~⁷, wherein said first signal combining circuit comprises a multiplexor circuit that multiplexes said OSD signal and said first controlled signal in response to said first combining control signal to thereby provide said first combination signal.

~~11~~¹¹. (ORIGINAL) The apparatus of claim ~~12~~⁷, wherein said second signal combining circuit comprises a multiplexor circuit that multiplexes said second and third controlled signals in response to said second combining control signal to thereby provide said multiplexed signal.

~~12~~¹². (ORIGINAL) The apparatus of claim ~~12~~⁷, wherein said variable filter circuit comprises a variable high pass filter circuit.

~~13~~¹³. (ORIGINAL) The apparatus of claim ~~12~~⁷, wherein said variable filter circuit comprises:

an amplifier circuit; and

a variably capacitive feedback circuit, coupled to said amplifier circuit, that in response to a portion of said plurality of filter control signals provides a variably capacitive feedback for said amplifier circuit.

~~14~~¹⁴. (ORIGINAL) The apparatus of claim ~~13~~¹³, wherein said variably capacitive feedback circuit comprises:

a plurality of capacitive circuit elements;

a plurality of switch circuits, coupled between said amplifier circuit and said plurality of capacitive circuit elements, that in response to said portion of said plurality of filter control signals selectively provide electrical connections between

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said amplifier circuit and said plurality of capacitive circuit elements.

¹⁵
~~20.~~ (ORIGINAL) The apparatus of claim ¹³
~~18~~, further comprising a
variable DC signal generator circuit, coupled to said variable filter circuit, that in
response to another portion of said plurality of filter control signals provides a
variable DC signal to said variably capacitive feedback circuit.

[21-23. (CANCELLED)

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~~24.~~ (ORIGINAL) A method of controlling and multiplexing video
image and on-screen-display (OSD) signals, comprising the steps of:

receiving a first reference signal, a contrast control signal and a clamped
video signal and in response thereto generating a first controlled signal with a
contrast-controlled video component;

receiving a first combining control signal and in response thereto receiving
and selectively combining an OSD signal and said first controlled signal and
thereby generating a first combination signal with said contrast-controlled video
component and an OSD component;

receiving said first combination signal, said first reference signal and a gain
control signal and in response thereto generating a second controlled signal with a
contrast-controlled and gain-controlled video component and a gain-controlled
OSD component;

receiving a second combining control signal and in response thereto
receiving and selectively combining said second controlled signal and a second
reference signal and thereby generating a multiplexed signal with said contrast-
controlled and gain-controlled video component, said gain-controlled OSD
component and a reference signal component; and

receiving a plurality of filter control signals and in response thereto

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selectively filtering said multiplexed signal and thereby generating a filtered multiplexed signal with said contrast-controlled and gain-controlled video component and said gain-controlled OSD component having enhanced high frequency signal magnitudes.

25-26. (CANCELLED)

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